

Notice of References CitedApplication No.
09/251,183Applicant(s)
LinExaminer
Joannie Adelle GarciaGroup Art Unit
2823

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	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	5,827,776 ✓	10-98	Bandyopadhyay et al	----	----
B	5,827,778 ✓	10-98	Yamada	----	----
C	5,910,020 ✓	6-99	Yamada	----	----
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N	4-316351 ✓	11-92	JP	Fujitso LTD	----	----
O	1-184848 ✓	7-89	JP	Agency Of Ind Sci Tech	----	----
P	1-184849 ✓	7-89	JP	Agency Of Ind Sci Tech	----	----
Q	1-135043 ✓	5-89	JP	Toshiba Corp	----	----
R	1-183836 /	7-89	JP	Sanyo Electric KK	----	----
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NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	S.Wolf, "Silicon Processing for the VLSI Era", Lattice Press, Pages 214-217, 229-233, and 235	1990
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